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APPLICATION NO.	F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/602,261	06/23/2003		Henry A. Om'Mani	X-978-1D US	1157
24309	7590	03/19/2004		EXAMINER	
XILINX, IN ATTN: LEG		A DTRACKIT	TAN, VIBOL		
2100 LOGIC		AKTMENT	ART UNIT	PAPER NUMBER	
SAN JOSE, CA 95124				2819	
				DATE MAILED: 03/19/2004	1

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)					
	10/602,261	OM'MANI ET AL.					
Office Action Summary	Examiner	Art Unit					
	Vibol Tan	2819					
The MAILING DATE of this communication ap Period for Reply	pears on the cover sheet with the c	orrespondence address					
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a rep If NO period for reply is specified above, the maximum statutory period Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailine earned patent term adjustment. See 37 CFR 1.704(b).		nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).					
Status							
1)⊠ Responsive to communication(s) filed on 23 J	lune 2003.						
	s action is non-final.						
3) Since this application is in condition for allows	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims							
4) Claim(s) 1-4 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) Claim(s) is/are allowed. 6) Claim(s) 1 and 4 is/are rejected. 7) Claim(s) 2 and 3 is/are objected to. 8) Claim(s) are subject to restriction and/or election requirement.							
Application Papers							
9) The specification is objected to by the Examin 10) The drawing(s) filed on is/are: a) acc Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the E	cepted or b) objected to by the lead rawing(s) be held in abeyance. See ction is required if the drawing(s) is objection.	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).					
Priority under 35 U.S.C. § 119							
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority document application from the International Bureat* See the attached detailed Office action for a list	nts have been received. Its have been received in Applicationity documents have been received in the control of	on No ed in this National Stage					
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date 6/23/03.	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:						

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Art Unit: 2819

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.
- 2. Claims 1 and 4 are rejected under 35 U.S.C. 102(a) as being anticipated by applicants' admitted prior arts of Figs. 1A & 1B.

In claim 1, the applicants' admitted prior arts of Figs. 1A & 1B teaches all claimed features, a transmitter circuit (145) for transmitting a test signal (HVIN) from a programmable input/output pin (115) of a programmable logic device, the transmitter circuit comprising an amplifier (150) having an amplifier control terminal (CTRL), a first supply terminal (VFY) connected to the input/output pin, and a first amplifier output terminal (VFY_S).

In claim 4, the applicants' admitted prior arts of Figs. 1A & 1B further teaches, the transmitter circuit of claim 1, further comprising a transistor (155) having a first current-handling terminal (drain terminal) connected to the input/output pin (115), a second current-handling terminal (source terminal), and a control terminal (gate terminal) connected to the first amplifier output terminal (HV).

3. Claims 2 and 3 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Vibol Tan whose telephone number is (571) 272-1811. The examiner can normally be reached on Monday-Friday (7:00 AM-4:30 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mike J. Tokar can be reached on (571) 272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Vibol Tan

Primary Examiner, AU 2819